

J = chip has serial jtag connection to Service Processor HW

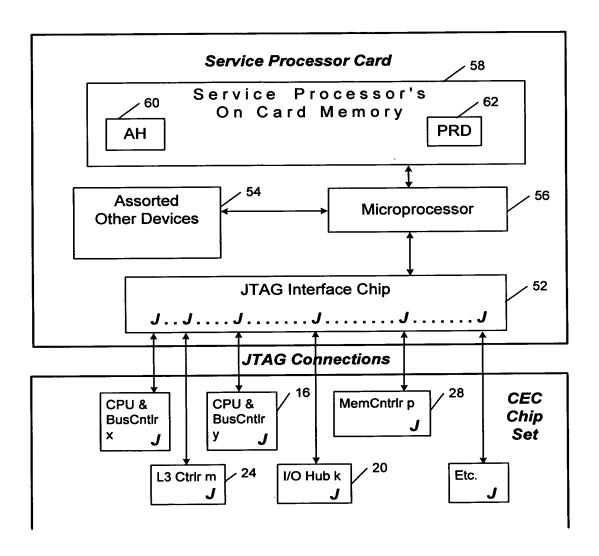
dotted box indicates remote chip or device inaccessible to SP via jtag

dotted line indicates connections to and between remote chips & devices

FIG. 1



<u>50</u>



J = JTAG serial interface between Service Processor & chips in CEC

FIG. 2



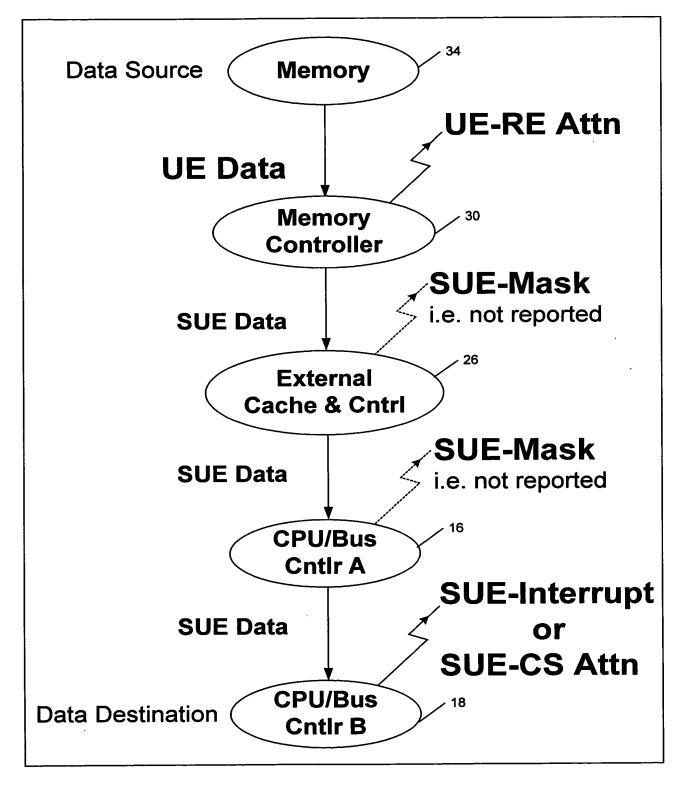


FIG. 3



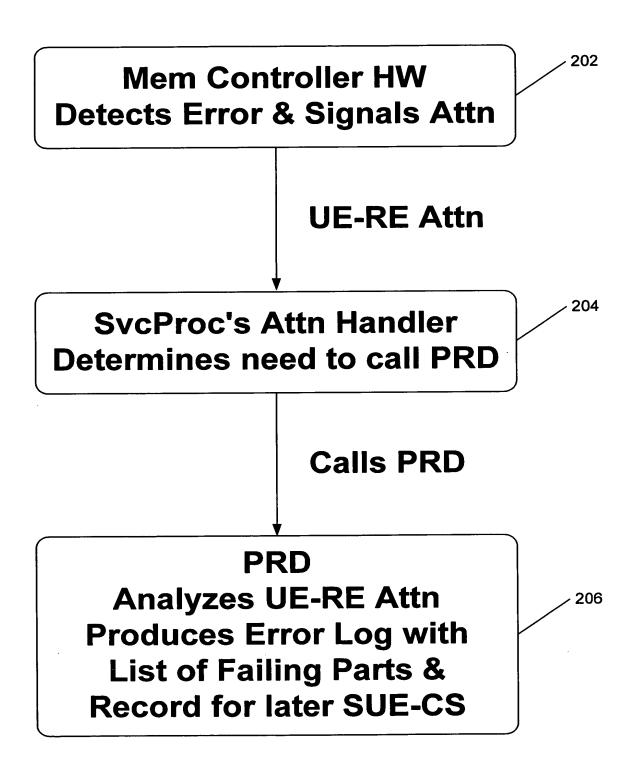


FIG. 4



302 **CPU/Bus Cntlr D HW Detects Error & Signals Attn SUE-CS Attn SvcProc's Attn Handler Determines need to call PRD** Calls PRD **PRD** 306 **Analyzes SUE-CS Attn Produces Error Log with List of Failing Parts.**

FIG. 5

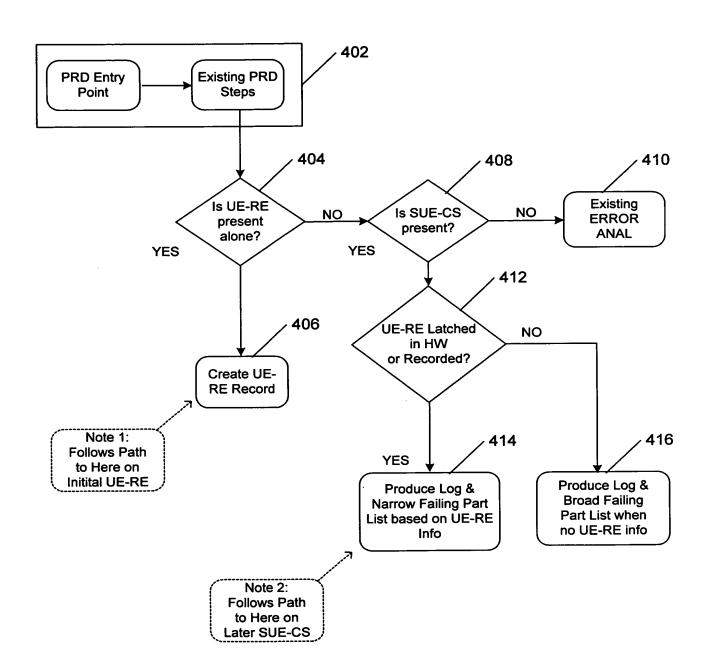


FIG. 6



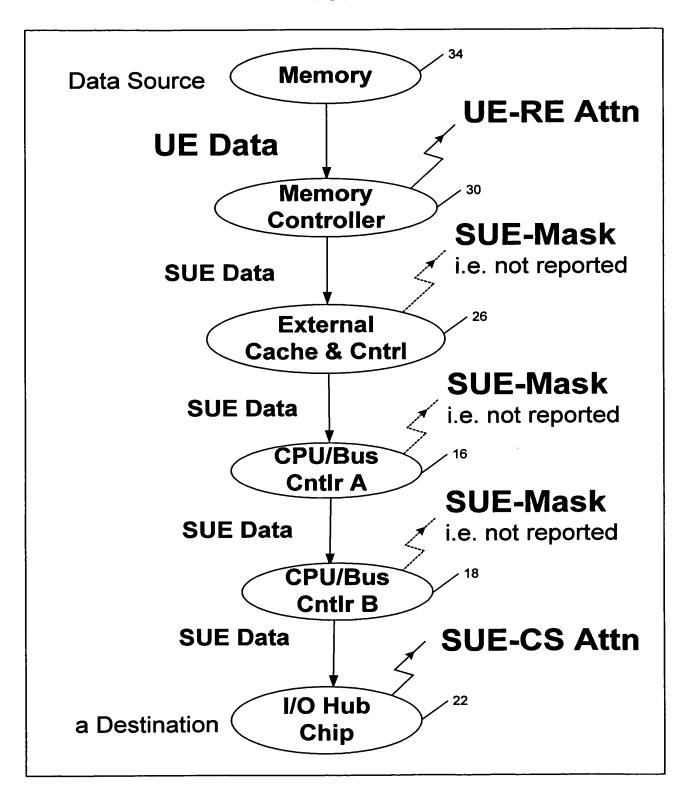


FIG. 7